

What is claimed is:

1. A semiconductor package comprising:

a package substrate;

5 a semiconductor chip attached to the substrate;

a TIM formed on the semiconductor chip;

a dam that substantially surrounds the TIM; and

a lid placed over the TIM to contact a surface thereof,

wherein the dam is formed on the substrate and in contact with the sides of the TIM

10 and the semiconductor chip, wherein the lid further comprises a supporter for supporting the dam, and wherein the lid comprises injection holes through which materials required to form the dam and TIM are injected.

2. The semiconductor package of claim 1, wherein the lid is attached to the

15 substrate via a sealant and covers the semiconductor chip and the dam.

3. The semiconductor package of claim 1, wherein the dam is formed of a material that is not liquefied at least below about 125°C.

20 4. The semiconductor package of claim 3, wherein the material that is not liquefied below about 125°C comprises a thermosetting epoxy.

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